

LEMBAR
HASIL PENILAIAN SEJAWAT SEBIDANG ATAU *PEER REVIEW*
KARYA ILMIAH : JURNAL ILMIAH

Judul Jurnal Ilmiah (Artikel) : Design of Digital Parity Generator Layout using 0.7 micron Technology

Penulis Jurnal Ilmiah : Wahab Musa, Sri Wahyuni Dali, Ade Irawaty Tolago

Identitas Jurnal Ilmiah :

- a. Nama Jurnal : International Journal of Electrical and Computer Engineering (IJECE)
- b. Nomor/Volume : No.5, Vol.8
- c. Edisi (bulan/tahun) : October 2018
- d. Nomor ISSN : 2088-8708
- e. DOI artikel : 10.11591/ijece.v8i5.pp3550-3559
- f. Penerbit : <http://iaescore.com/journals/index.php/IJECE>
- g. Jumlah halaman : 10
- h. Terindeks di : SCOPUS (Q2)

Kategori Publikasi Jurnal Ilmiah (beri ✓ pada kategori yang tepat) :

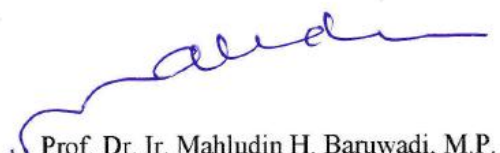
- ☒ Jurnal Ilmiah Internasional
- ☐ Jurnal Ilmiah Nasional Terakreditasi
- ☐ Jurnal Ilmiah Nasional Tidak Terakreditasi

Hasil Penilaian *Peer Review* :

Komponen Yang Dinilai	Nilai Maksimal Jurnal Ilmiah: 40			Nilai Akhir Yang Diperoleh
	Internasional ☒	Nasional Terakreditasi ☐	Nasional Tidak Terakreditasi ☐	
a. Kelengkapan unsur isi buku (10%)	4			3
b. Ruang lingkup dan kedalaman pembahasan (30%)	12			9
c. Kecukupan dan kemutakhiran data/informasi dan metodologi (30%)	12			8
d. Kelengkapan unsur dan kualitas penerbit (30%)	12			9
Total = (100%)	40			29
Nilai Pengusul = (60%)	24			17.4
Catatan penilaian artikel oleh Reviewer 1: - Perluas cakupan pembandingan pada hasil dengan teknologi yang sama - Coba melakukan analisa hasil dengan tidak melibatkan shift register untuk luas area layout				

9 Nopember 2018

Reviewer 1


 Prof. Dr. Ir. Mahludin H. Baruwadi, M.P.
 NIP. 19650711199103 1 003
 Unit kerja : Universitas Negeri Gorontalo

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	Internasional ☒	Nasional Terakreditasi ☐	Nasional Tidak Terakreditasi ☐	
e. Kelengkapan unsur isi buku (10%)	4			3,5
f. Ruang lingkup dan kedalaman pembahasan (30%)	12			10
g. Kecukupan dan kemutahiran data/informasi dan metodologi (30%)	12			7,5
h. Kelengkapan unsur dan kualitas penerbit (30%)	12			7
Total = (100%)	40			28
Nilai Pengusul = (60%)	24			16.8
Catatan penilaian artikel oleh Reviewer 2: - Kurangi uraian mengenai landasan teori, dan lebih fokus ke diskusi hasil dan analisa - Peningkatan analisa hasil untuk luas area layout rangkaian Parity Generator saja tanpa SR-8 bit agar fair dibandingkan dengan yang menggunakan teknologi Quantum Cellular Automata				

9 Nopember 2018

Reviewer 2



Dr. Sardi Salim, M.Pd
 NIP. 19680705 199702 1 001
 Unit kerja : Teknik Elektro UNG

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	Internasional ☒	Nasional Terakreditasi ☐	Nasional Tidak Terakreditasi ☐	Reviewer PTN	TIM PAK Pusat
i. Kelengkapan unsur isi buku (10%)	4			3.25	
j. Ruang lingkup dan kedalaman pembahasan (30%)	12			9.5	
k. Kecukupan dan kemutakhiran data/informasi dan metodologi (30%)	12			7.75	
l. Kelengkapan unsur dan kualitas penerbit (30%)	12			8	
Total = (100%)	40			28.5	
Nilai Pengusul = (60%)	24			17.1	
Catatan penilaian artikel oleh Tim PAK Pusat:					

9 Nopember 2018

TIM PAK Pusat,

Submit Manuscript

2 messages

Wahab Musa <wmusa@ung.ac.id>
To: ijece@iaesjournal.com

23 October 2017 at 09:40

This is the first of my manuscript for IJECE, I hope that published on up coming volume. The title is

Design of Digital Parity Generator Layout Using 0.7 micron Technology

Thank You
Wahab Musa



Design of Digital Parity Generator Layout Using 0.7 Micron Technology-2.docx
283K

IJECE Journal <ijece@iaesjournal.com>
To: wmusa@ung.ac.id

23 October 2017 at 09:40

Dear Authors, Reviewers, Readers or Partners

Thank you for contacting us. We confirm that your email has reached us. We will reply to you as soon as possible.

For Authors: Thank you for considering *the International Journal of Electrical and Computer Engineering (IJECE)*, ISSN: 2088-8708 (**Scopus** indexed journal) as the right place for your work. You should **ONLY** submit your papers using [our online submission system](#) (**submissions by email will be ignored**). Please ensure that you have prepared your manuscripts in line with *the IJECE's Author Guidelines* (where there are further relevant instructions that you must read and follow to ensure your submission goes smoothly). A good research paper has a clear statement of the problem the paper is addressing, the proposed solution(s), and results achieved. It describes clearly what has been done before on the problem, and what is new. All submissions will be reviewed by three independent reviewers and refereed by the Editors. Submissions will be judged on the following criteria: *Relevance, Significance, Novelty, Technical correctness, Experimental/evidential support, Clarity of presentation and Reference to prior work and publications.*

How to submit your manuscript?

To make a submission, you must have a user account and **be enrolled as an Author**. User accounts can either be created by the Journal Manager or you can [register yourself](#) (this journal policy allow you create user account by your self as a Reader, an Author and/or a Reviewer). All fields with an asterisk beside them (Username; Password; Repeat Password; First Name; Last Name; Email) are mandatory. Your username and your email address must be unique; furthermore, while you can change your email address at a later date, you will be unable to change your username. If you want to register in another role within the same journal (for example, if you are already a Reader, but also want to become an Author) you can log in; go to Edit My Profile (under My Account on your User Home page); and check off the checkboxes next to any available roles, near the bottom of the page. Once you have an account, log in to the journal site and select the role of Author.

Your cooperation is very appreciated.

Best Regards,
T. Sutikno

Managing Editor, International Journal of Electrical and Computer Engineering (IJECE)

ISSN: 2088-8708 (a Scopus indexed journal)

<http://www.iaescore.com/journals/index.php/IJECE> (NEW online papers submission)

<http://www.iaesjournal.com/online/index.php/IJECE>

email: ijece@iaesjournal.com

[IJECE] Submission Acknowledgement

1 message

Tole Sutikno <ijece@iaesjournal.com>
To: Hi Wahab Musa <wmusa@ung.ac.id>

23 October 2017 at 10:1

The following message is being delivered on behalf of International Journal of Electrical and Computer Engineering (IJECE).

Hi Wahab Musa:

Thank you for submitting the manuscript, "Design of Digital Parity Generator Layout Using 0.7 micron Technology" to International Journal of Electrical and Computer Engineering (IJECE). With the online journal management system that we are using, you will be able to track its progress through the editorial process by logging in to the journal web site:

Manuscript URL:

<http://www.iaescore.com/journals/index.php/IJECE/author/submission/9462>

Username: wmusa

If you have any questions, please contact me. Thank you for considering this journal as a venue for your work.

Tole Sutikno

International Journal of Electrical and Computer Engineering (IJECE)

International Journal of Electrical and Computer Engineering (IJECE)

<http://www.iaescore.com/journals/index.php/IJECE>

Editor/Author Correspondence

Edit [DELETE](#)

or Subject: [IJECE] Editor Decision

2017 The following message is being delivered on behalf of International
-12- Journal of Electrical and Computer Engineering (IJECE).
21

12:4

0 PM

Hi Wahab Musa:

We have reached a decision regarding your submission to International Journal of Electrical and Computer Engineering (IJECE), "Design of Digital Parity Generator Layout Using 0.7 micron Technology".

Our decision is: Revisions Required

Please read our instructions (at:

<http://www.iaescore.com/journals/index.php/IJECE/about/editorialPolicies#custom-3>) carefully and follow the "checklist for preparing your paper for publication" strictly, as any spelling mistakes and errors may be translated into the typeset version.

MOST IMPORTANT:

1. The process publication will be smooth and fast only if you have prepared your revised paper carefully and strictly with considering the checklist above.
2. You should improve your analyzing and also present the comparison between performance of your approach and other researches. The "result and discussion" section reports the most important findings, including analysing results as appropriate.
3. All references must be cited in your article, and CITATION ORDER is SEQUENTIAL [1], [2], [3], [4],

4. Enrich your references with recent related journal articles. The references must be integrated with not less than three (3) papers published on IAES's Journals:

- <http://iaescore.com/journals/index.php/ijece>
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Please use "Search" at menu "Journal Content" in right side of the site

When your revised paper reached us, it will be evaluated for contribution, originality, relevance, and presentation. In order to shorten the review process you should submit your revised paper

within 8 weeks. Then, your paper will be judged: accepted or rejected.

We look forward to hearing from you

Thank you,

Sincerely yours,
T. Sutikno
Managing Editor

International Journal of Electrical and Computer Engineering (IJECE)
<http://www.iaescore.com/journals/index.php/IJECE>



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On Mon, Jan 29, 2018 at 7:41 PM, Tole Sutikno <ijece@iaesjournal.com> wrote:

The following message is being delivered on behalf of International Journal of Electrical and Computer Engineering (IJECE).

T. Sutikno:

A revised version of "Design of Digital Parity Generator Layout Using 0.7 micron Technology" has been uploaded by the author Hi Wahab Musa.

Submission URL:

<http://www.iaescore.com/journals/index.php/IJECE/editor/submissionReview/9462>

Tole Sutikno

International Journal of Electrical and Computer Engineering (IJECE)

International Journal of Electrical and Computer Engineering (IJECE)

<http://www.iaescore.com/journals/index.php/IJECE>



Wahab Musa <wmusa@ung.ac.id>

Re: [IJECE] Revised Version Uploaded

3 messages

IJECE Journal <ijece@iaesjournal.com>

18 February 2018 at 22:43

To: Hi Wahab Musa <wmusa@ung.ac.id>

We are not interested to publish paper with single author
thank

Wahab Musa <wmusa@ung.ac.id>

21 February 2018 at 10:51

To: IJECE Journal <ijece@iaesjournal.com>

A revised version of "Design of Digital Parity Generator Layout Using 0.7 micron Technology" included co-authors
have been uploaded.

Thanks

[Quoted text hidden]

IJECE Journal <ijece@iaesjournal.com>

21 February 2018 at 10:51

To: wmusa@ung.ac.id

Dear Authors, Reviewers, Readers or Partners

Thank you for contacting us. We confirm that your email has reached us. We will reply to you as soon as possible.

For Authors: Thank you for considering *the International Journal of Electrical and Computer Engineering (IJECE)*, ISSN: 2088-8708 (**Scopus** indexed journal) as the right place for your work. You should ONLY submit your papers using [our online submission system](#) (**submissions by email will be ignored**). Please ensure that you have prepared your manuscripts in line with *the IJECE's Author Guidelines* (where there are further relevant instructions that you must read and follow to ensure your submission goes smoothly). A good research paper has a clear

statement of the problem the paper is addressing, the proposed solution(s), and results achieved. It describes clearly what has been done before on the problem, and what is new. All submissions will be reviewed by three independent reviewers and refereed by the Editors. Submissions will be judged on the following criteria: *Relevance, Significance, Novelty, Technical correctness, Experimental/evidential support, Clarity of presentation and Reference to prior work and publications.*

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Your cooperation is very appreciated.

Best Regards,

T. Sutikno

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ISSN: 2088-8708 (a Scopus indexed journal)

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We really appreciate your total commitment to supporting this journal.

Thank you

Best Regards,

T. Sutikno

Editor

IJECE@iaesjournal.com

<http://www.iaesjournal.com/online/index.php/IJECE>

On Wed, Feb 21, 2018 at 9:45 AM, Tole Sutikno <ijece@iaesjournal.com> wrote:

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T. Sutikno:

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Submission URL:

<http://www.iaescore.com/journals/index.php/IJECE/editor/submissionReview/9462>

Re: [IJECE] Revised Version Uploaded

1 message

IJECE Journal <ijece@iaesjournal.com>

22 May 2018 at 05:55

To: Hi Wahab Musa <wmusa@ung.ac.id>

Dear Professor,

It is my great pleasure to inform you that your paper is accepted and will be published on forthcoming issue of the International Journal of Electrical and Computer Engineering (IJECE), a Scopus indexed journal. Congratulations!

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