

PROSES SUBMISSION:  
Pada 23 October 2017



Wahab Musa <wmusa@ung.ac.id>

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## Submit Manuscript

2 messages

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Wahab Musa <wmusa@ung.ac.id>  
To: ijece@iaesjournal.com

23 October 2017 at 09:40

This is the first of my manuscript for IJECE, I hope that published on up coming volume. The title is

### **Design of Digital Parity Generator Layout Using 0.7 micron Technology**

Thank You  
Wahab Musa



**Design of Digital Parity Generator Layout Using 0.7 Micron Technology-2.docx**  
283K

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IJECE Journal <ijece@iaesjournal.com>  
To: wmusa@ung.ac.id

23 October 2017 at 09:40

Dear Authors, Reviewers, Readers or Partners

Thank you for contacting us. We confirm that your email has reached us. We will reply to you as soon as possible.

**For Authors:** Thank you for considering *the International Journal of Electrical and Computer Engineering (IJECE)*, ISSN: 2088-8708 (**Scopus** indexed journal) as the right place for your work. You should **ONLY** submit your papers using [our online submission system](#) (**submissions by email will be ignored**). Please ensure that you have prepared your manuscripts in line with [the IJECE's Author Guidelines](#) (where there are further relevant instructions that you must read and follow to ensure your submission goes smoothly). A good research paper has a clear statement of the problem the paper is addressing, the proposed solution(s), and results achieved. It describes clearly what has been done before on the problem, and what is new. All submissions will be reviewed by three independent reviewers and refereed by the Editors. Submissions will be judged on the following criteria: *Relevance, Significance, Novelty, Technical correctness, Experimental/evidential support, Clarity of presentation and Reference to prior work and publications.*

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Your cooperation is very appreciated.

Best Regards,  
T. Sutikno

Managing Editor, International Journal of Electrical and Computer Engineering (IJECE)  
ISSN: 2088-8708 (a Scopus indexed journal)  
<http://www.iaescore.com/journals/index.php/IJECE> (NEW online papers submission)  
<http://www.iaesjournal.com/online/index.php/IJECE>  
email: [ijece@iaesjournal.com](mailto:ijece@iaesjournal.com)

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## [IJECE] Submission Acknowledgement

1 message

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**Tole Sutikno** <ijece@iaesjournal.com>  
To: Hi Wahab Musa <wmusa@ung.ac.id>

23 October 2017 at 10:1

The following message is being delivered on behalf of International Journal of Electrical and Computer Engineering (IJECE).

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Hi Wahab Musa:

Thank you for submitting the manuscript, "Design of Digital Parity Generator Layout Using 0.7 micron Technology" to International Journal of Electrical and Computer Engineering (IJECE). With the online journal management system that we are using, you will be able to track its progress through the editorial process by logging in to the journal web site:

Manuscript URL:

<http://www.iaescore.com/journals/index.php/IJECE/author/submission/9462>

Username: wmusa

If you have any questions, please contact me. Thank you for considering this journal as a venue for your work.

Tole Sutikno  
International Journal of Electrical and Computer Engineering (IJECE)

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International Journal of Electrical and Computer Engineering (IJECE)  
<http://www.iaescore.com/journals/index.php/IJECE>

PROSES REVIEW:

Pada 21-Desember-2017

## Editor/Author Correspondence

Edit [DELETE](#)

or Subject: [IJECE] Editor Decision

2017 The following message is being delivered on behalf of International  
-12- Journal of Electrical and Computer Engineering (IJECE).  
21

12:4

0 PM

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Hi Wahab Musa:

We have reached a decision regarding your submission to  
International Journal of Electrical and Computer Engineering (IJECE),  
"Design of Digital Parity Generator Layout Using 0.7 micron  
Technology".

Our decision is: Revisions Required

Please read our instructions (at:

<http://www.iaescore.com/journals/index.php/IJECE/about/editorialPolicies#custom-3>) carefully and follow the "checklist for preparing your paper for publication" strictly, as any spelling mistakes and errors may be translated into the typeset version.

### MOST IMPORTANT:

1. The process publication will be smooth and fast only if you have prepared your revised paper carefully and strictly with considering the checklist above.

2. You should improve your analyzing and also present the comparison between performance of your approach and other researches. The "result and discussion" section reports the most important findings, including analysing results as appropriate.

3. All references must be cited in your article, and CITATION ORDER is SEQUENTIAL [1], [2], [3], [4], .....

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Please use "Search" at menu "Journal Content" in right side of the site

When your revised paper reached us, it will be evaluated for contribution, originality, relevance, and presentation. In order to shorten the review process you should submit your revised paper

within 8 weeks. Then, your paper will be judged: accepted or rejected.

We look forward to hearing from you

Thank you,

Sincerely yours,  
T. Sutikno  
Managing Editor

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International Journal of Electrical and Computer Engineering (IJECE)  
<http://www.iaescore.com/journals/index.php/IJECE>

PROSES REVISI:  
Pada 29 Januari 2018



Virus-free. [www.avg.com](http://www.avg.com)

On Mon, Jan 29, 2018 at 7:41 PM, Tole Sutikno <[ijece@iaesjournal.com](mailto:ijece@iaesjournal.com)> wrote:

The following message is being delivered on behalf of International Journal of Electrical and Computer Engineering (IJECE).

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T. Sutikno:

A revised version of "Design of Digital Parity Generator Layout Using 0.7 micron Technology" has been uploaded by the author Hi Wahab Musa.

Submission URL:

<http://www.iaescore.com/journals/index.php/IJECE/editor/submissionReview/9462>

Tole Sutikno  
International Journal of Electrical and Computer Engineering (IJECE)

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International Journal of Electrical and Computer Engineering (IJECE)  
<http://www.iaescore.com/journals/index.php/IJECE>

Proses Revisi-ke 2 (Lanjutan)  
Pada 18 February 2018



Wahab Musa <wmusa@ung.ac.id>

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**Re: [IJECE] Revised Version Uploaded**

3 messages

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**IJECE Journal** <ijece@iaesjournal.com>  
To: Hi Wahab Musa <wmusa@ung.ac.id>

18 February 2018 at 22:43

We are not interested to publish paper with single author  
thank

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Upload Revisi ke-2:  
Pada 21 February 2018

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**Wahab Musa** <wmusa@ung.ac.id>  
To: IJECE Journal <ijece@iaesjournal.com>

21 February 2018 at 10:51

A revised version of "Design of Digital Parity Generator Layout Using 0.7 micron Technology" included co-authors  
have been uploaded.

Thanks

[Quoted text hidden]

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**IJECE Journal** <ijece@iaesjournal.com>  
To: wmusa@ung.ac.id

21 February 2018 at 10:51

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statement of the problem the paper is addressing, the proposed solution(s), and results achieved. It describes clearly what has been done before on the problem, and what is new. All submissions will be reviewed by three independent reviewers and refereed by the Editors. Submissions will be judged on the following criteria: *Relevance, Significance, Novelty, Technical correctness, Experimental/evidential support, Clarity of presentation and Reference to prior work and publications.*

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Your cooperation is very appreciated.

Best Regards,

T. Sutikno

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email: [ijece@iaesjournal.com](mailto:ijece@iaesjournal.com)

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Submit your FINAL camera ready paper as soon

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We really appreciate your total commitment to supporting this journal.

Thank you

Best Regards,

T. Sutikno

Editor

[IJECE@iaesjournal.com](mailto:IJECE@iaesjournal.com)

<http://www.iaesjournal.com/online/index.php/IJECE>

On Wed, Feb 21, 2018 at 9:45 AM, Tole Sutikno <[ijece@iaesjournal.com](mailto:ijece@iaesjournal.com)> wrote:

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T. Sutikno:

A revised version of "Design of Digital Parity Generator Layout Using 0.7 micron Technology" has been uploaded by the author Hi Wahab Musa.

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PROSES UPLOAD CAMERA READY PAPER

PROSES ACCEPTED  
Pada 22 Mei 2018



Wahab Musa <wmusa@ung.ac.id>

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**Re: [IJECE] Revised Version Uploaded**

1 message

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**IJECE Journal** <ijece@iaesjournal.com>  
To: Hi Wahab Musa <wmusa@ung.ac.id>

22 May 2018 at 05:55

Dear Professor,

It is my great pleasure to inform you that your paper is accepted and will be published on forthcoming issue of the International Journal of Electrical and Computer Engineering (IJECE), a Scopus indexed journal. Congratulations!

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